

Electronic Acknowledgement Receipt

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Title of Invention:	Integrated Redundancy Architecture and Method for Providing Redundancy Allocation to an Embedded Memory System
First Named Inventor/Applicant Name:	Wayne F. Ellis
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Filer:	Morgan Heller/Karen Jeffer
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		Response2Final02_04_09.pdf	392180 b1058b4541a533998a15aed41abd014f23386213	yes	8

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Amendment After Final	1	1
	Claims	2	5
	Applicant Arguments/Remarks Made in an Amendment	6	8
Warnings:			
Information:			
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